

FIGURE 1 (PRIOR ART)

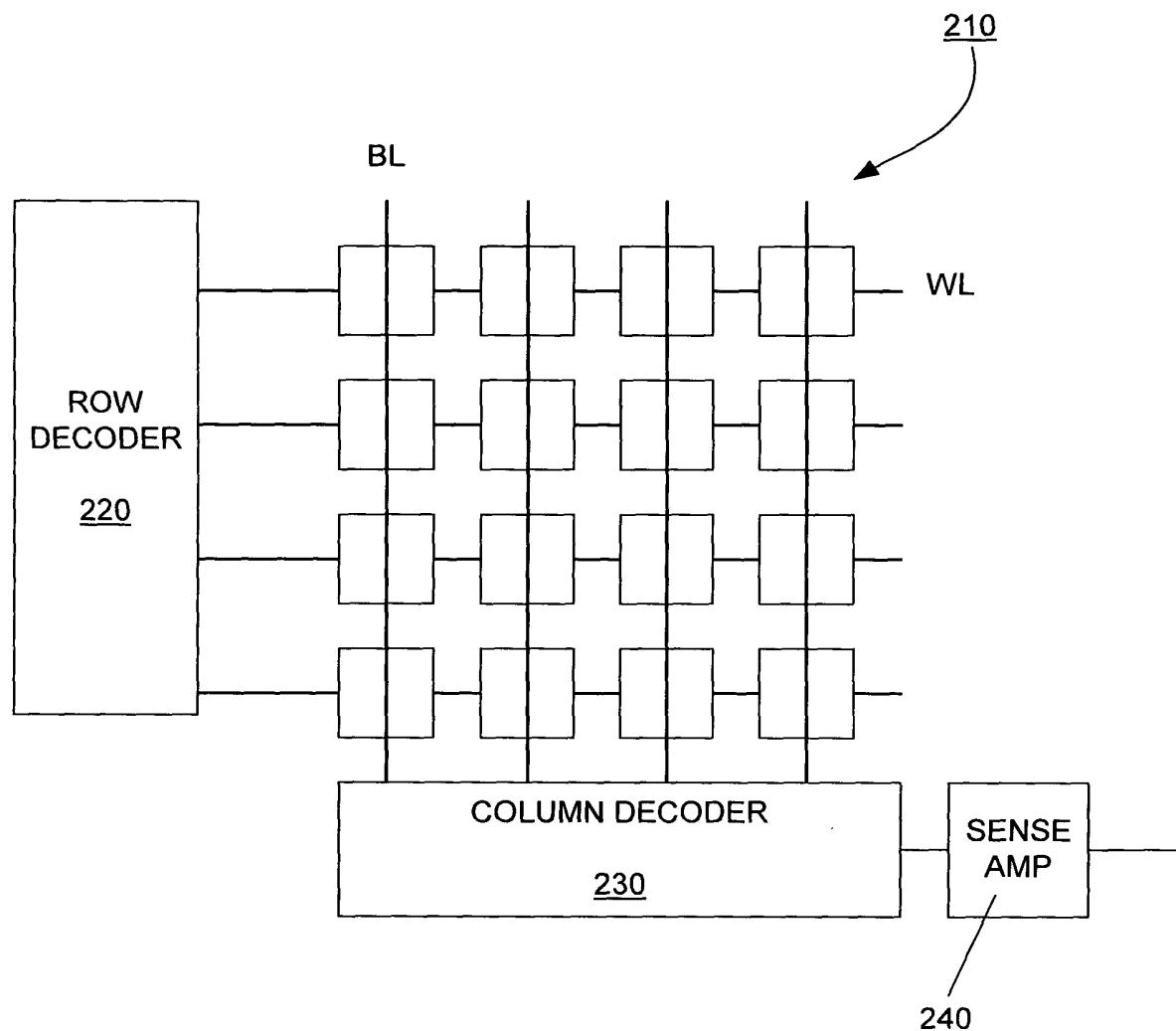


FIGURE 2 (PRIOR ART)

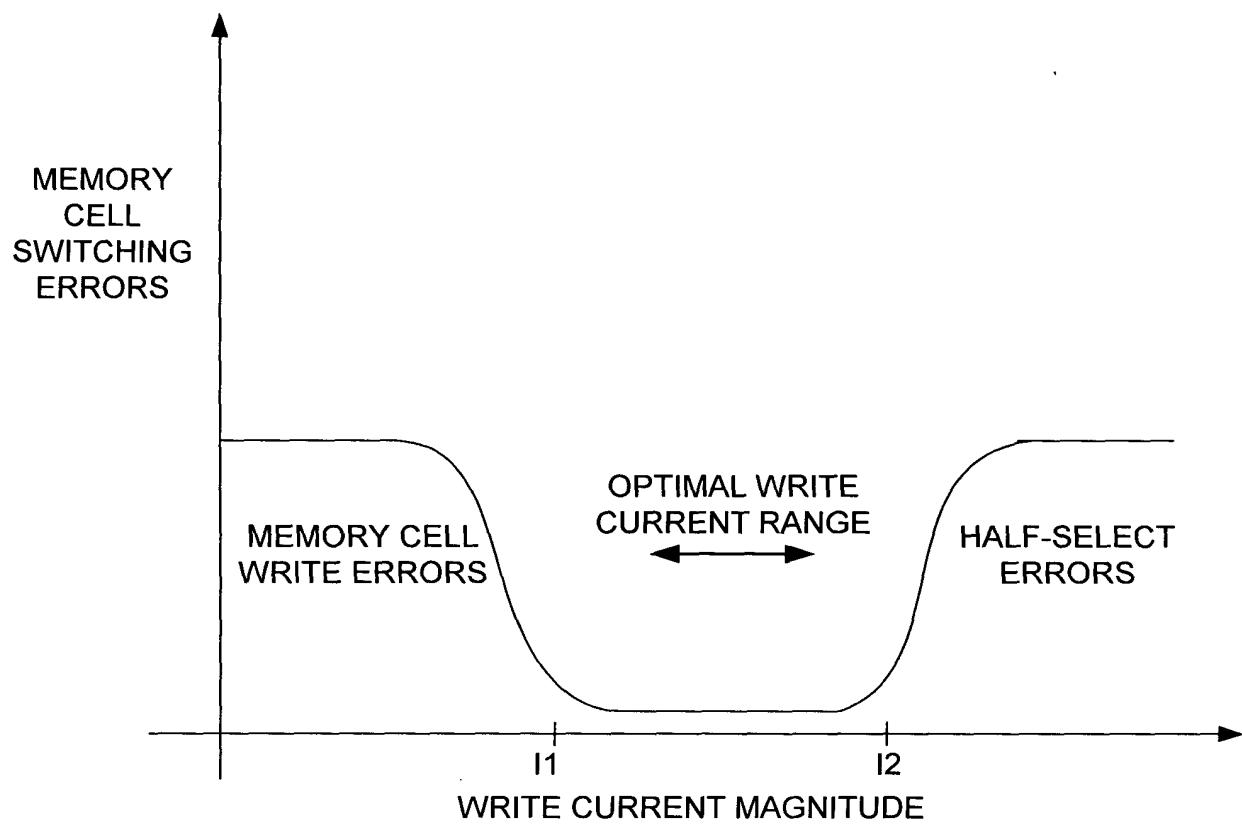


FIGURE 3

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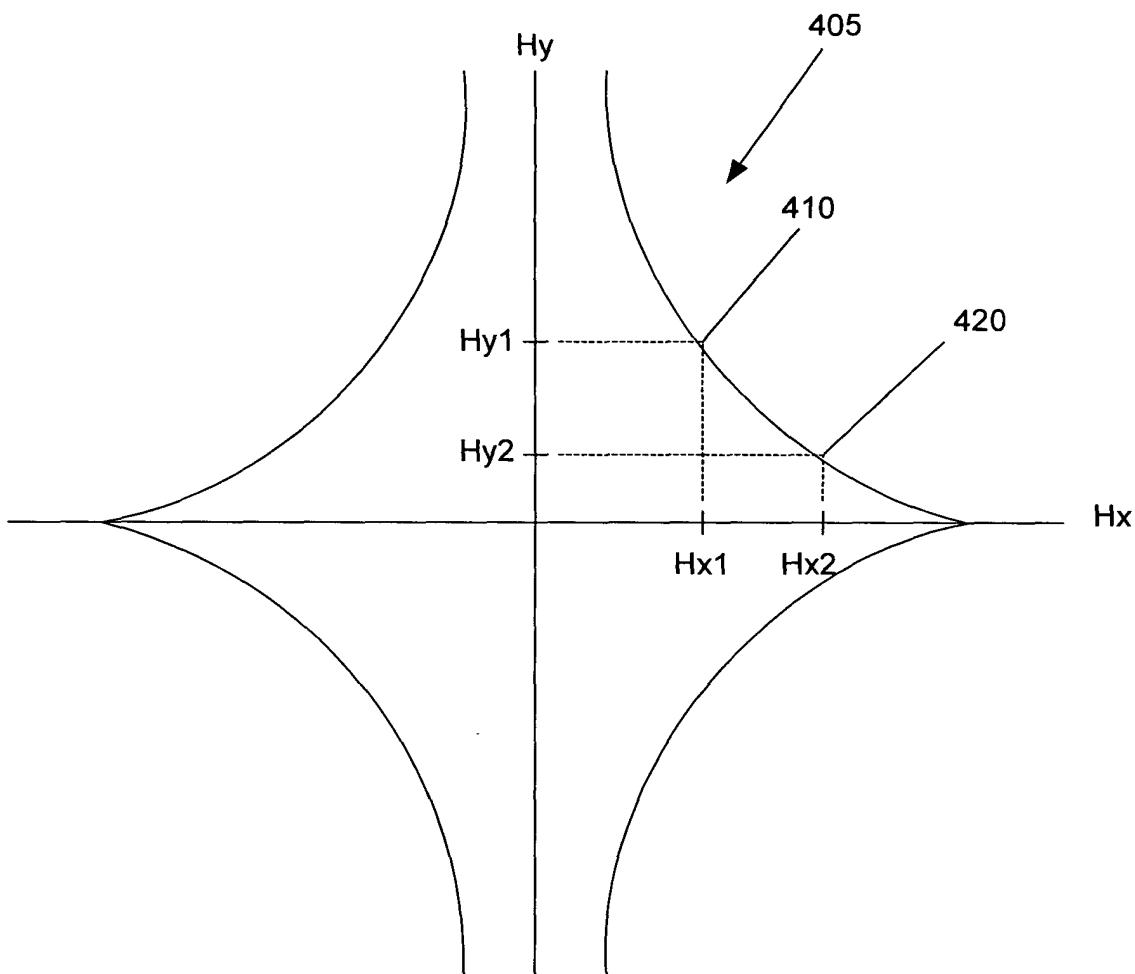


FIGURE 4

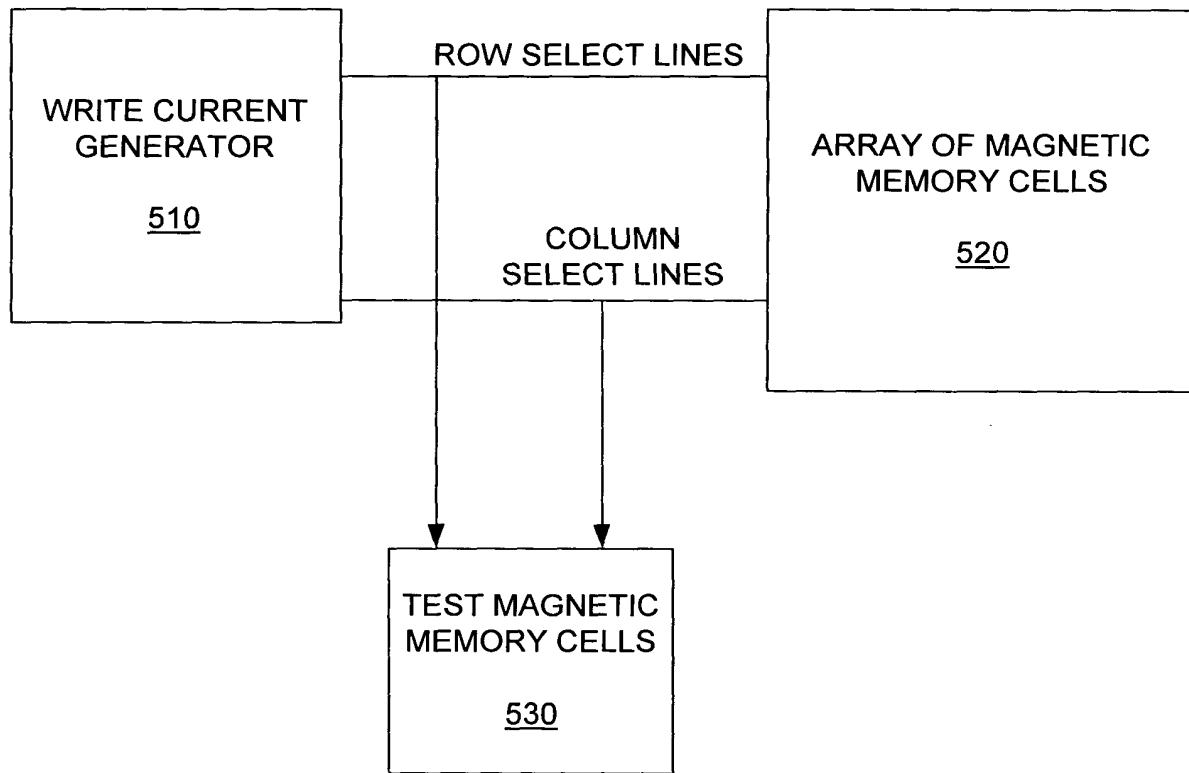


FIGURE 5

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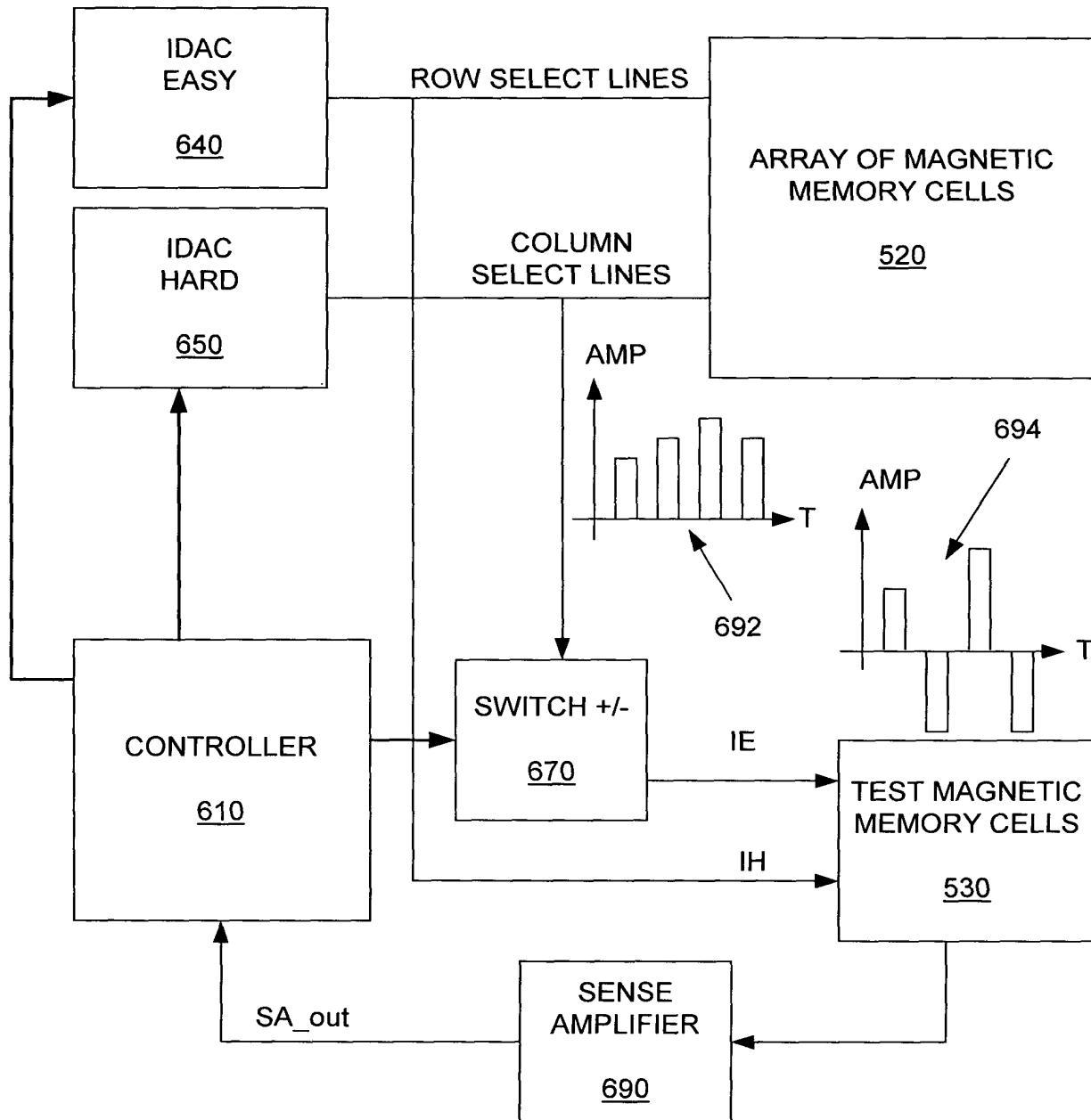


FIGURE 6

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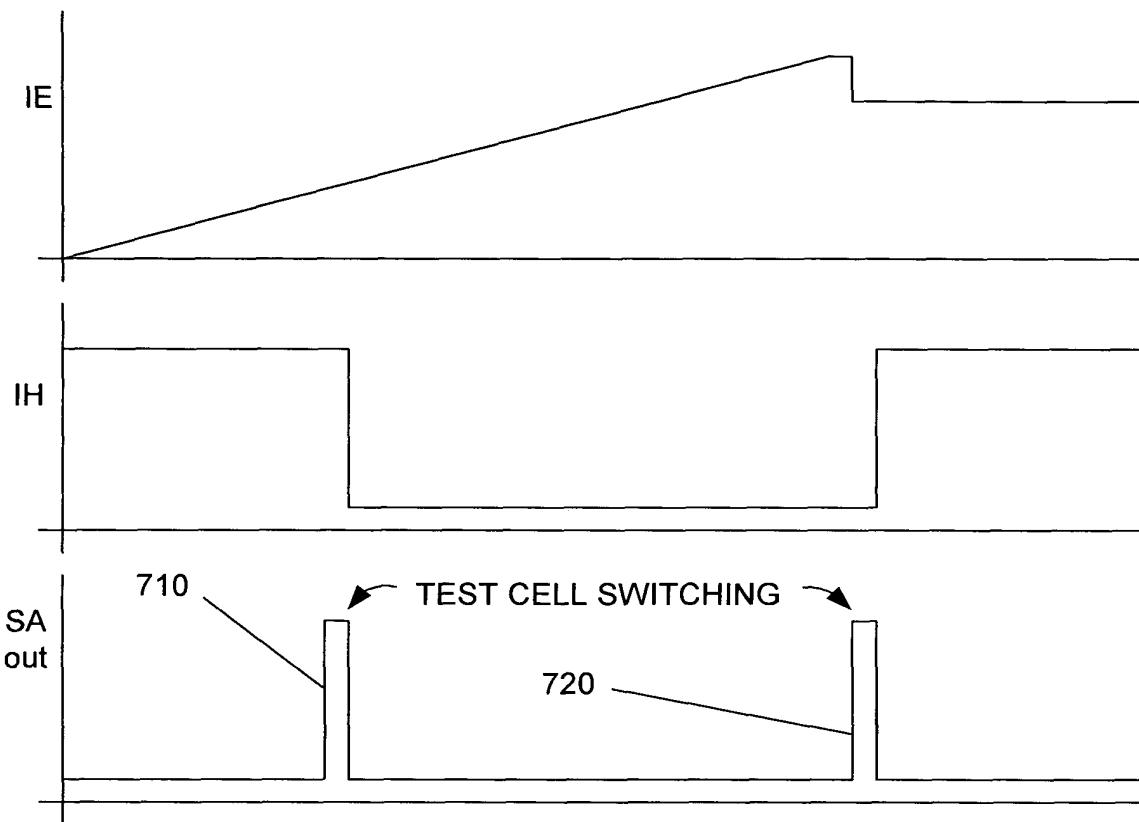


FIGURE 7

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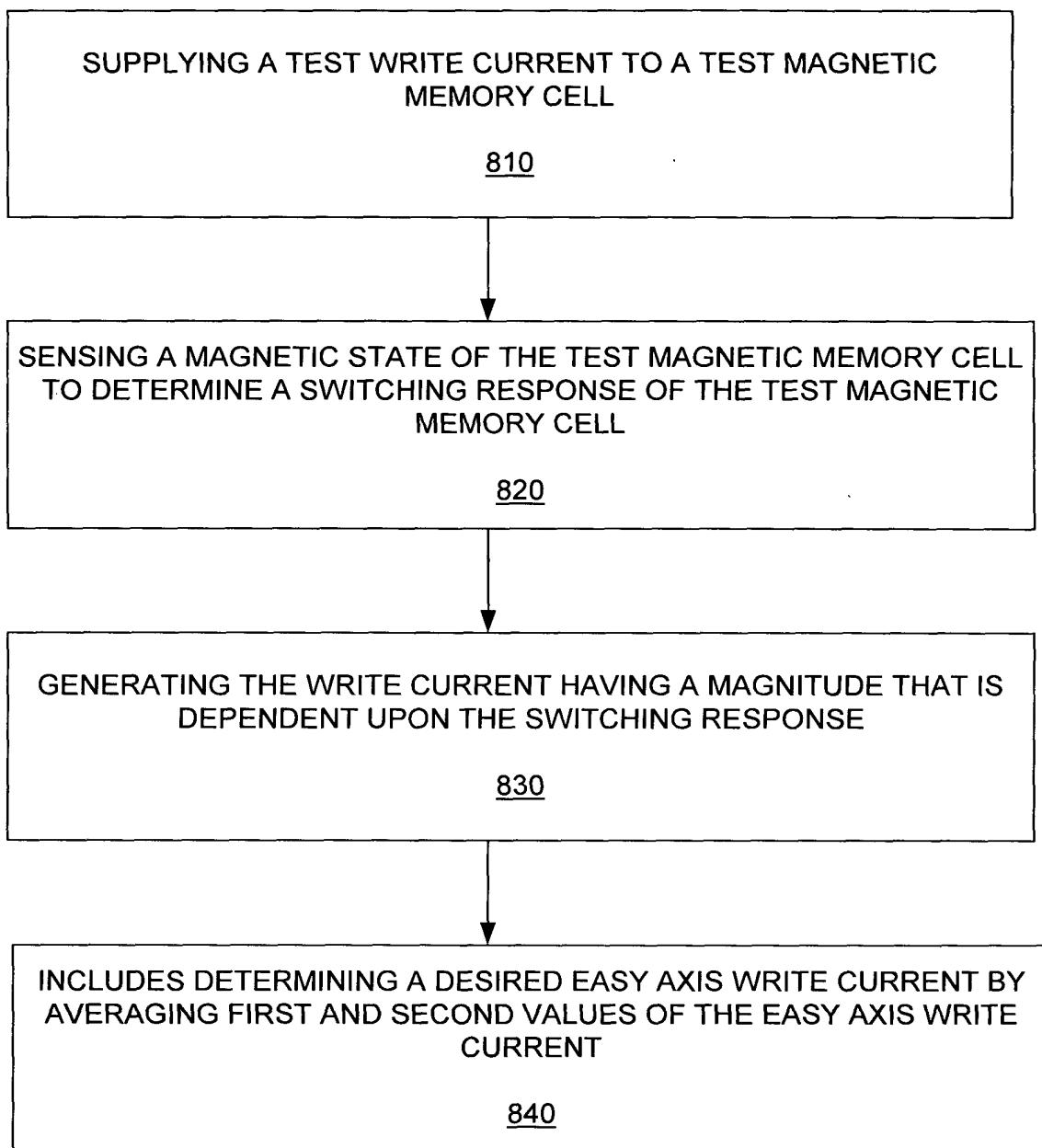


FIGURE 8

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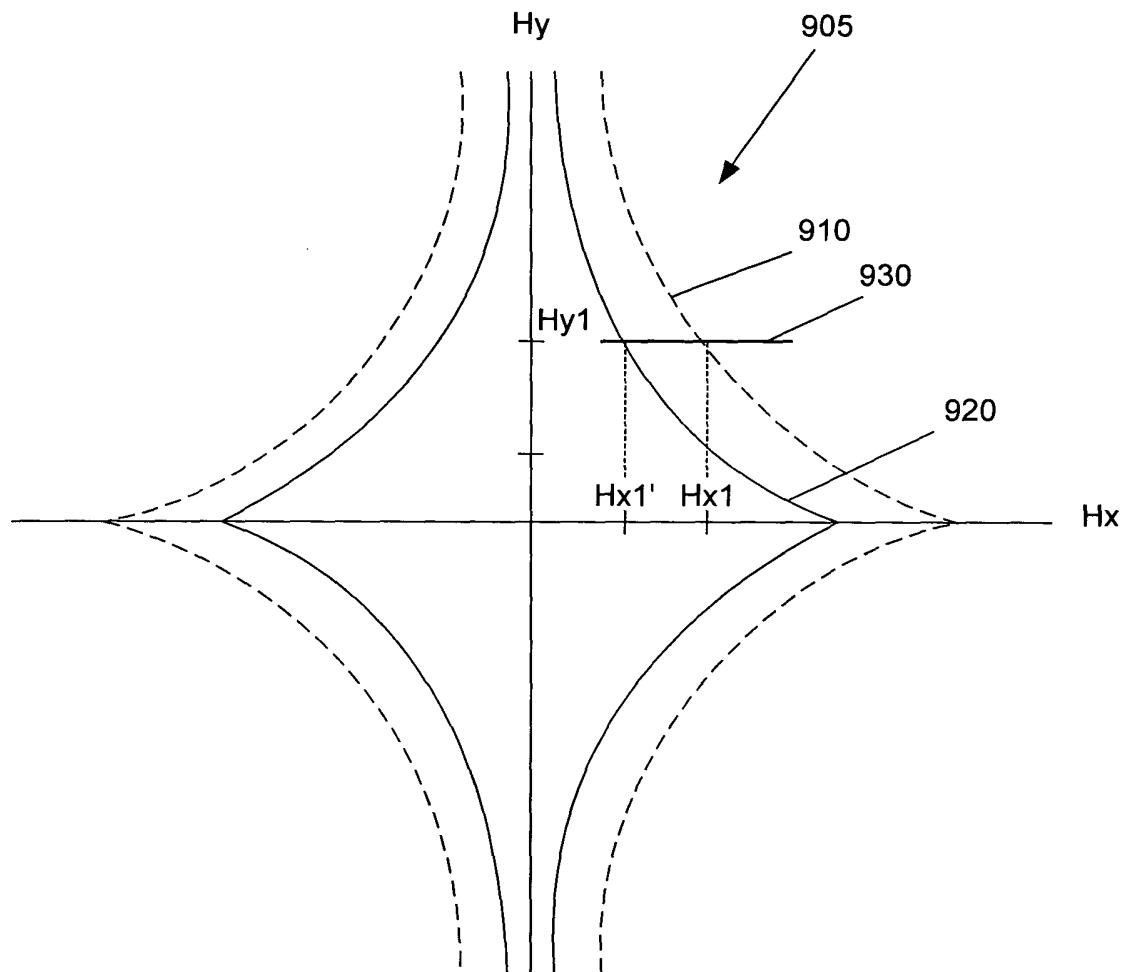


FIGURE 9

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SUBTRACTING A DELTA CURRENT FROM THE EASY AXIS WRITE CURRENT WHILE MAINTAINING THE HARD AXIS WRITE CURRENT AT A DEFAULT VALUE

1010

VERIFYING THAT THE WRITE CURRENT IS STILL ADEQUATE FOR SWITCHING THE AT LEAST ONE TEST MAGNETIC MEMORY CELL

1020

IF THE WRITE CURRENT IS NOT ADEQUATE TO SWITCH THE AT LEAST ONE TEST MAGNETIC MEMORY CELL, THEN INCREMENTING THE EASY AXIS WRITE CURRENT UNTIL THE WRITE CURRENT IS ADEQUATE TO SWITCHING THE AT LEAST ONE TEST MAGNETIC MEMORY CELL

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SETTING THE HARD AXIS WRITE CURRENT TO THE DEFAULT VALUE, AND SETTING THE EASY AXIS WRITE CURRENT TO THE INCREMENTED EASY AXIS WRITE CURRENT VALUE PLUS THE DELTA CURRENT

1040

FIGURE 10A

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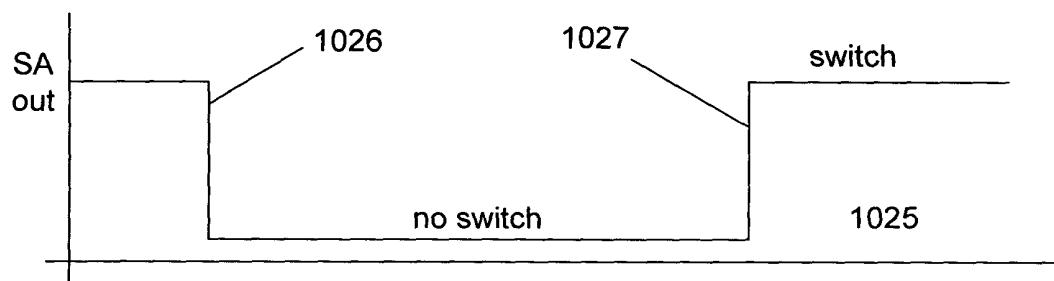
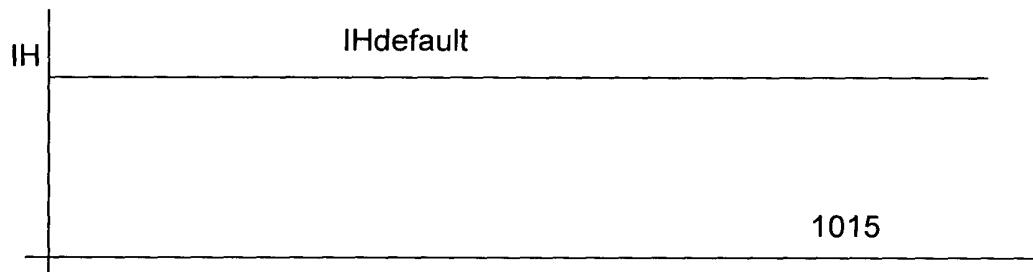
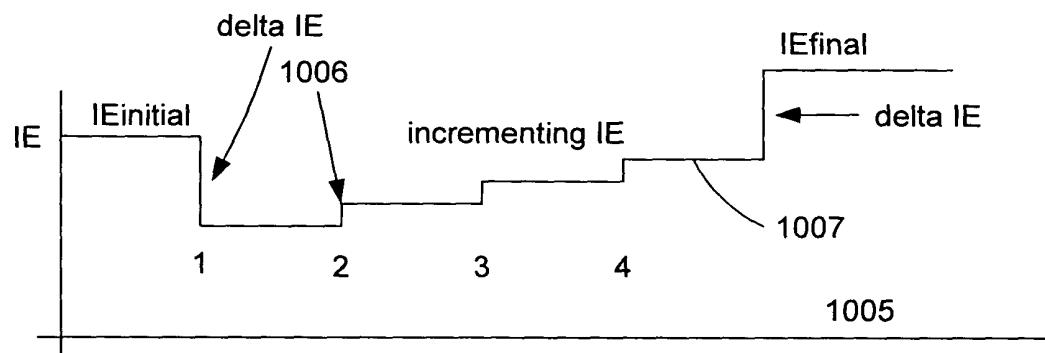


FIGURE 10B

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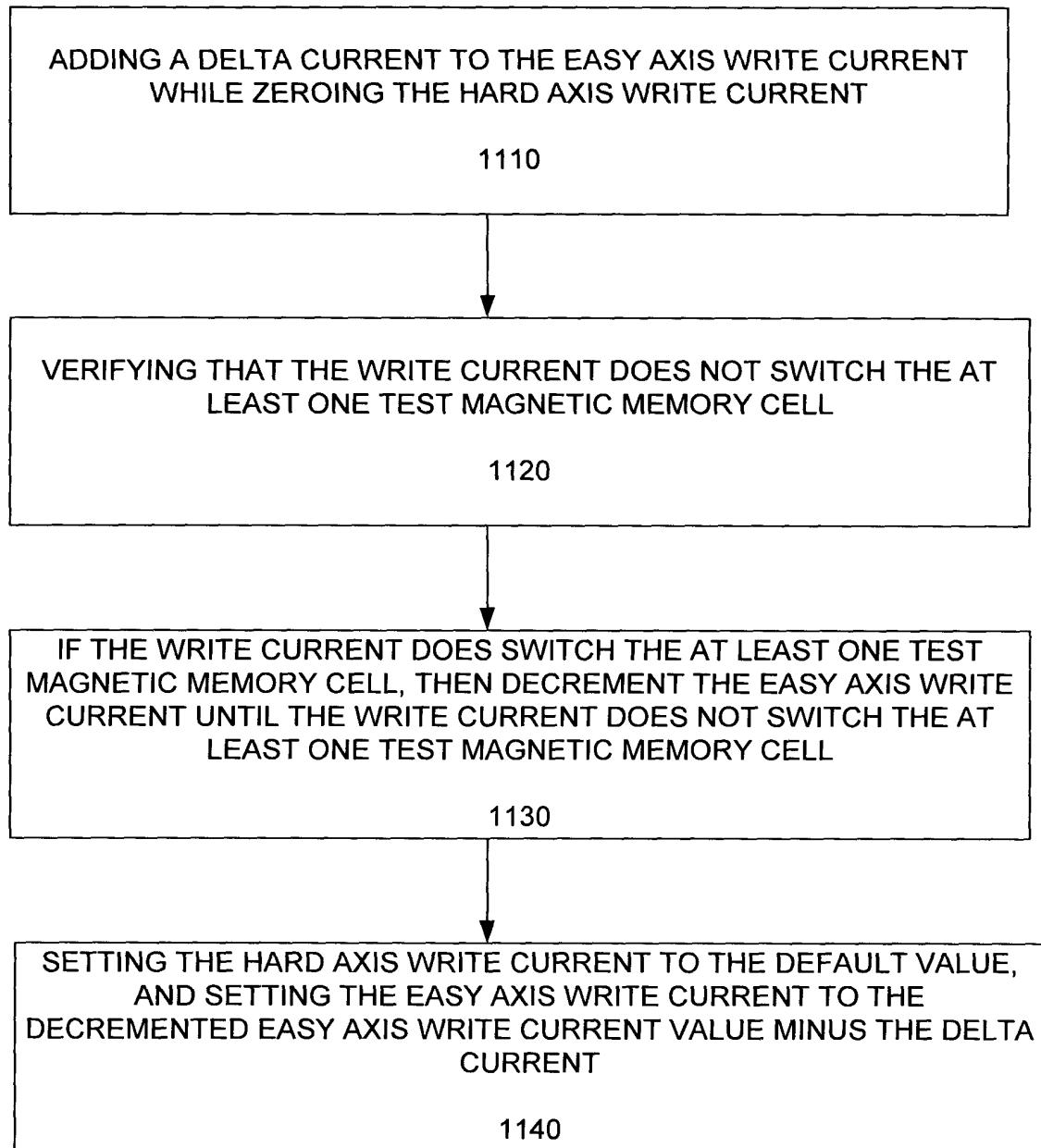


FIGURE 11A

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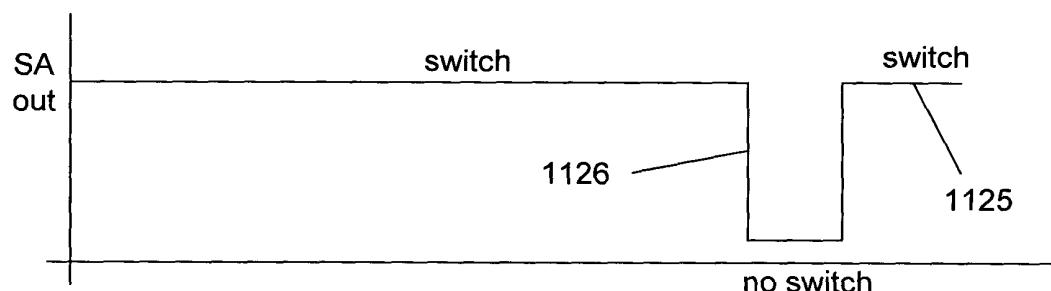
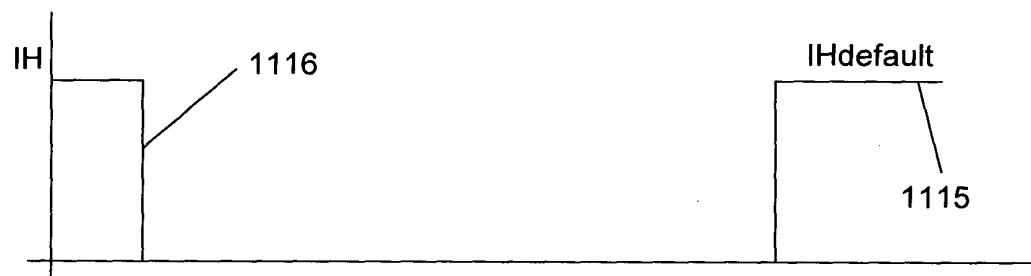
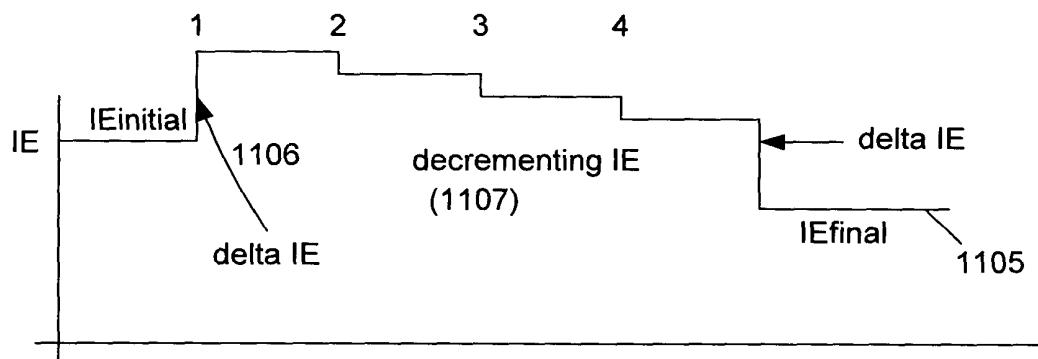


FIGURE 11B